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BUCKLEY ALYSON

Integrated Circuit and System Design MIT Press

This book presents selected papers from the 3rd International Conference on Micro-Electronics and Telecommunication Engineering, held at SRM Institute of Science and Technology, Ghaziabad, India, on 30-31 August 2019. It covers a wide variety of topics in micro-electronics and telecommunication engineering, including micro-electronic engineering, computational remote sensing, computer science and intelligent systems, signal and image processing, and information and communication technology.

Field Programmable Logic and Application John Wiley & Sons

This book constitutes the refereed proceedings of the 12th International Conference on Field-Programmable Logic and Applications, FPL 2002, held in Montpellier, France, in September 2002. The 104 revised regular papers and 27 poster papers presented together with three invited contributions were carefully reviewed and selected from 214 submissions. The papers are organized in topical sections on rapid prototyping, FPGA synthesis, custom computing engines, DSP applications, reconfigurable fabrics, dynamic reconfiguration, routing and placement, power estimation, synthesis issues, communication applications, new technologies, reconfigurable architectures, multimedia applications, FPGA-based arithmetic, reconfigurable processors, testing and fault-tolerance, crypto applications, multitasking, compilation techniques, etc.

Digital Circuit Analysis and Design with Simulink Modeling and Introduction to CPLDs and FPGAs Springer Nature

The seventh book in the CHDL Series is composed of a selection of the best articles from the Forum on Specification and Design

Languages (FDL'04). FDL is the European Forum to learn and exchange on new trends on the application of languages and models for the design of electronic and heterogeneous systems. The forum was structured around four workshops that are all represented in the book by outstanding articles: Analog and Mixed-Signal Systems, UML-based System Specification and Design, C/C++-Based System Design and Languages for Formal Specification and Verification. The Analog and Mixed-Signal Systems contributions bring some answers to the difficult problem of co-simulating discrete and continuous models of computation. The UML-based System Specification and Design chapters bring insight into how to use the Model Driven Engineering to design Systems-on-Chip. The C/C++-Based System Design articles mainly explore system level design with SystemC. The Languages for Formal Specification and Verification is represented by an invited contribution on the use of temporal assertions for symbolic model checking and simulation. And finally chapter in this book contributed by preeminent members of the automotive design industry presents the recent industry standard AutoSAR. Overall Advances in Design and Specification Languages for SoCs is an excellent opportunity to catch up with the latest research developments in the field of languages for electronic and heterogeneous system design.

Digital Design (VHDL) Springer Science & Business Media
Field-Programmable Gate Arrays (FPGAs) are revolutionizing digital signal processing as novel FPGA families are replacing ASICs and PDSs for front-end digital signal processing algorithms. So the efficient implementation of these algorithms is critical and is the main goal of this book. It starts with an overview of today's FPGA technology, devices, and tools for designing state-of-the-art DSP systems. A case study in the first chapter is the basis for more than 30 design examples

throughout. The following chapters deal with computer arithmetic concepts, theory and the implementation of FIR and IIR filters, multirate digital signal processing systems, DFT and FFT algorithms, and advanced algorithms with high future potential. Each chapter contains exercises. The VERILOG source code and a glossary are given in the appendices, while the accompanying CD-ROM contains the examples in VHDL and Verilog code as well as the newest Altera "Baseline" software. This edition has a new chapter on adaptive filters, new sections on division and floating point arithmetics, an up-date to the current Altera software, and some new exercises.

Design Automation, Languages, and Simulations Singh Publication
This book includes high-quality research papers presented at the Third International Conference on Innovative Computing and Communication (ICICC 2020), which is held at the Shaheed Sukhdev College of Business Studies, University of Delhi, Delhi, India, on 21-23 February, 2020. Introducing the innovative works of scientists, professors, research scholars, students and industrial experts in the field of computing and communication, the book promotes the transformation of fundamental research into institutional and industrialized research and the conversion of applied exploration into real-time applications.

Advances in Design and Specification Languages for SoCs
BoD - Books on Demand

Power Efficiency in Broadband Wireless Communications focuses on the improvement of power efficiency in wireless communication systems, especially of mobile devices. Reviewing cutting-edge techniques for conserving power and boosting power efficiency, the book examines various technologies and their impact on consumer devices. It considers each technology, first by introducing the main physical layer components in recent wireless communication systems along with their shortcomings,

and then proposing solutions for overcoming these shortcomings. The book covers orthogonal frequency division multiplexing (OFDM) signal generation and formulation and examines the advantages and disadvantages of OFDM systems compared to alternative multiplexing. It introduces one of the main drawbacks of OFDM systems, peak-to-average power ratio (PAPR), and discusses several PAPR techniques. It also explains how to overcome the main drawbacks of real-world OFDM system applications. Considers power amplifier linearization for increasing power efficiency and reducing system costs and power dissipation. Describes the implementation scenario of the most promising linearization technique, digital predistortion. Presents some experimental demonstrations of digital predistortion when the device under test is in the loop. Because the most costly device in a communication system that has a direct impact on power efficiency and power consumption is the power amplifier, the book details the behavior and characteristics of different classes of power amplifiers. Describing the evolution of the mobile cellular communication system, it details a cost-effective technique to help you increase power efficiency, reduce system costs, and prolong battery life in next generation mobile devices. *Field-programmable Logic and Applications* Springer Science & Business Media

The main purpose of this book is to provide a modern review about recent advances in Fourier transforms as the most powerful analytical tool for high-tech application in electrical, electronic, and computer engineering, as well as Fourier transform spectral techniques with a wide range of biological, biomedical, biotechnological, pharmaceutical, and nanotechnological applications. The confluence of Fourier transform methods with high tech opens new opportunities for detection and handling of atoms and molecules using nanodevices, with potential for a large variety of scientific and technological applications.

Circuit Design with VHDL Springer Nature

Welcome to the proceedings of PATMOS 2004, the fourteenth in a series of international workshops. PATMOS 2004 was organized by the University of Patras with technical co-sponsorship from the IEEE Circuits and Systems Society. Over the years, the PATMOS meeting has evolved into an important European event, where industry and academia meet to discuss power and timing aspects in modern integrated circuit and system design. PATMOS provides

a forum for researchers to discuss and investigate the emerging challenges in design methodologies and tools required to develop the upcoming generations of integrated circuits and systems. We realized this vision this year by providing a technical program that contained state-of-the-art technical contributions, a keynote speech, three invited talks and two embedded tutorials. The technical program focused on timing, performance and power consumption, as well as architectural aspects, with particular emphasis on modelling, design, characterization, analysis and optimization in the nanometer era. This year a record 152 contributions were received to be considered for presentation at PATMOS. Despite the choice for an intense three-day meeting, only 51 lecture papers and 34 poster papers could be accommodated in the single-track technical program. The Technical Program Committee, with the assistance of additional expert reviewers, selected the 85 papers to be presented at PATMOS and organized them into 13 technical sessions. As was the case with the PATMOS workshops, the review process was anonymous, full papers were required, and several reviews were received per manuscript.

Digital Signal Processing with Field Programmable Gate Arrays Springer Science & Business Media

This book constitutes the refereed proceedings of the 6th International Workshop on Systems, Architectures, Modeling, and Simulation, SAMOS 2006, held in Samos, Greece on July 2006. The 47 revised full papers presented together with 2 keynote talks were thoroughly reviewed and selected from 130 submissions. The papers are organized in topical sections on system design and modeling, wireless sensor networks, processor design, dependable computing, architectures and implementations, and embedded sensor systems.

High-Performance Computing Using FPGAs Springer

An integrated presentation of electronic circuit design and VHDL, with an emphasis on system examples and laboratory exercises.

Reconfigurable N-Point FFT Processors Springer Science & Business Media

This textbook introduces readers to the fundamental hardware used in modern computers. The only pre-requisite is algebra, so it can be taken by college freshman or sophomore students or even used in Advanced Placement courses in high school. This book presents both the classical approach to digital system design (i.e.,

pen and paper) in addition to the modern hardware description language (HDL) design approach (computer-based). This textbook enables readers to design digital systems using the modern HDL approach while ensuring they have a solid foundation of knowledge of the underlying hardware and theory of their designs. This book is designed to match the way the material is actually taught in the classroom. Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics. The author has designed the content with learning goals and assessment at its core. Each section addresses a specific learning outcome that the learner should be able to "do" after its completion. The concept checks and exercise problems provide a rich set of assessment tools to measure learner performance on each outcome. This book can be used for either a sequence of two courses consisting of an introduction to logic circuits (Chapters 1-7) followed by logic design (Chapters 8-13) or a single, accelerated course that uses the early chapters as reference material.

Recent Trends in Electronics and Communication Springer Nature

This book comprises select proceedings of the International Conference on VLSI, Communication and Signal processing (VCAS 2020). The contents are broadly divided into three topics - VLSI, Communication, and Signal Processing. The book focuses on the latest innovations, trends, and challenges encountered in the different areas of electronics and communication, especially in the area of microelectronics and VLSI design, communication systems and networks, and image and signal processing. It also offers potential solutions and provides an insight into various emerging areas such as Internet of Things (IoT), System on a Chip (SoC), Sensor Networks, underwater and underground communication networks etc. This book will be useful for academicians and professionals alike.

VHDL Answers to Frequently Asked Questions Springer Science & Business Media

This book suggests and describes a number of fast parallel circuits for data/vector processing using FPGA-based hardware accelerators. Three primary areas are covered: searching, sorting, and counting in combinational and iterative networks. These include the application of traditional structures that rely on comparators/swappers as well as alternative networks with a

variety of core elements such as adders, logical gates, and look-up tables. The iterative technique discussed in the book enables the sequential reuse of relatively large combinational blocks that execute many parallel operations with small propagation delays. For each type of network discussed, the main focus is on the step-by-step development of the architectures proposed from initial concepts to synthesizable hardware description language specifications. Each type of network is taken through several stages, including modeling the desired functionality in software, the retrieval and automatic conversion of key functions, leading to specifications for optimized hardware modules. The resulting specifications are then synthesized, implemented, and tested in FPGAs using commercial design environments and prototyping boards. The methods proposed can be used in a range of data processing applications, including traditional sorting, the extraction of maximum and minimum subsets from large data sets, communication-time data processing, finding frequently occurring items in a set, and Hamming weight/distance counters/comparators. The book is intended to be a valuable support material for university and industrial engineering courses that involve FPGA-based circuit and system design.

Scientific and Technical Aerospace Reports Pearson Education India

Lists citations with abstracts for aerospace related reports obtained from world wide sources and announces documents that have recently been entered into the NASA Scientific and Technical Information Database.

Vhdl Coding Styles And Methodologies, 2E Springer Science & Business Media

Provides students with a system-level perspective and the tools they need to understand, analyze and design complete digital systems using VHDL. It goes beyond the design of simple combinational and sequential modules to show how such modules are used to build complete systems, reflecting digital design in the real world.

Micro-Electronics and Telecommunication Engineering

Springer

Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits. The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small examples, which are easy to understand. Then, a modular approach is used to show how larger circuits are designed. VHDL is a complex language so it is introduced gradually in the book. Each VHDL feature is presented as it becomes pertinent for the circuits being discussed. While it includes a discussion of VHDL, the book provides thorough coverage of the fundamental concepts of logic circuit design, independent of the use of VHDL and CAD tools. A CD-ROM containing all of the VHDL design examples used in the book, as well as Altera's Quartus II CAD software, is included free with every text.

A Fast Fourier Transform Radix-2 Complex Butterfly with Built-in Self-test Elsevier

A completely updated and expanded comprehensive treatment of VHDL and its applications to the design and simulation of real, industry-standard circuits. This comprehensive treatment of VHDL and its applications to the design and simulation of real, industry-standard circuits has been completely updated and expanded for the third edition. New features include all VHDL-2008 constructs, an extensive review of digital circuits, RTL analysis, and an unequalled collection of VHDL examples and exercises. The book focuses on the use of VHDL rather than solely on the language, with an emphasis on design examples and laboratory exercises. The third edition begins with a detailed review of digital circuits (combinatorial, sequential, state machines, and FPGAs), thus providing a self-contained single reference for the teaching of digital circuit design with VHDL. In its coverage of VHDL-2008, it makes a clear distinction between VHDL for synthesis and VHDL for simulation. The text offers complete VHDL codes in examples as well as simulation results and comments. The significantly expanded examples and exercises include many not previously

published, with multiple physical demonstrations meant to inspire and motivate students. The book is suitable for undergraduate and graduate students in VHDL and digital circuit design, and can be used as a professional reference for VHDL practitioners. It can also serve as a text for digital VLSI in-house or academic courses.

Fourier Transform Springer

As the complexity of electronic systems continues to increase, the micro-electronic industry depends upon automation and simulations to adapt quickly to market changes and new technologies. Compiled from chapters contributed to CRC's best-selling VLSI Handbook, this volume of the Principles and Applications in Engineering series covers a broad range of VHDL Coding Styles and Methodologies BoD - Books on Demand This book constitutes the refereed proceedings of the 8th International Workshop on Systems, Architectures, Modeling, and Simulation, SAMOS 2008, held in Samos, Greece, in July 2008. The 24 revised full papers presented together with a contemplative keynote and additional papers of two special workshop sessions were carefully reviewed and selected from 62 submissions. The papers are organized in topical sections on architecture, new frontiers, SoC, application specific contributions, system level design for heterogeneous systems, programming multicores, sensors and sensor networks; and systems modeling and design. Embedded Computer Systems: Architectures, Modeling, and Simulation Springer

The volume presents high quality research papers presented at Second International Conference on Information and Communication Technology for Intelligent Systems (ICICC 2017). The conference was held during 2-4 August 2017, Pune, India and organized communally by Dr. Vishwanath Karad MIT World Peace University, Pune, India at MIT College of Engineering, Pune and supported by All India Council for Technical Education (AICTE) and Council of Scientific and Industrial Research (CSIR). The volume contains research papers focused on ICT for intelligent computation, communications and audio, and video data processing.