

Field Programmable Gate Array Technology S Trimberger

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FPGAs BoD – Books on Demand

Many different kinds of FPGAs exist, with different programming technologies, different architectures and different software. Field-Programmable Gate Array Technology describes the major FPGA architectures available today, covering the three programming technologies that are in use and the major architectures built on those programming technologies. The reader is introduced to concepts relevant to the entire field of FPGAs using popular devices as examples. Field-Programmable Gate Array Technology includes discussions of FPGA integrated circuit manufacturing, circuit design and logic design. It describes the way logic and interconnect are implemented in various kinds of FPGAs. It covers particular problems with design for FPGAs and future possibilities for new architectures and software. This book compares CAD for FPGAs with CAD for traditional gate arrays. It describes algorithms for placement, routing and optimization of FPGAs. Field-Programmable Gate Array Technology describes all aspects of FPGA design and development. For this reason, it covers a significant amount of material. Each section is clearly explained to readers who are assumed to have general technical expertise in digital design and design tools. Potential developers of FPGAs will benefit primarily from the FPGA architecture and software discussion. Electronics systems designers and ASIC users will find a background to different types of FPGAs and applications of their use.

Field-Programmable Gate Arrays Tebbo

This book makes powerful Field Programmable Gate Array (FPGA) and reconfigurable technology accessible to software engineers by covering different state-of-the-art high-level synthesis approaches (e.g., OpenCL and several C-to-gates compilers). It introduces FPGA technology, its programming model, and how various applications can be implemented on FPGAs without going through low-level hardware design phases. Readers will get a realistic sense for problems that are suited for FPGAs and how to implement them from a software designer's point of view. The authors demonstrate that FPGAs and their programming model reflect the needs of stream processing problems much better than traditional CPU or GPU architectures, making them well-suited for a wide variety of systems, from embedded systems performing sensor processing to large setups for Big Data number crunching. This book serves as an invaluable tool for software designers and FPGA design engineers who are interested in high design productivity through behavioural synthesis, domain-specific compilation, and FPGA overlays. Introduces FPGA technology to software developers by giving an overview of FPGA programming models and design tools, as well as various application examples; Provides a holistic analysis of the topic and enables developers to tackle the architectural needs for Big Data processing with FPGAs; Explains the reasons for the energy efficiency and performance benefits of FPGA processing; Provides a user-oriented approach and a sense for where and how to apply FPGA technology.

Make: FPGAs CRC Press

Logic Synthesis and Optimization presents up-to-date research information in a pedagogical form. The authors are recognized as the leading experts on the subject. The focus of the book is on logic minimization and includes such topics as two-level minimization, multi-level minimization, application of binary decision diagrams, delay optimization, asynchronous circuits, spectral method for logic design, field programmable gate array (FPGA) design, EXOR logic synthesis and technology mapping. Examples and illustrations are included so that each contribution can be read independently. Logic Synthesis and Optimization is an indispensable reference for academic researchers as well as professional CAD engineers.

Rapid System Prototyping with FPGAs Morgan Kaufmann

This title serves as an introduction and reference for the field, with the papers that have shaped

the hardware/software co-design since its inception in the early 90s.

Nuclear Power Plant Instrumentation and Control Systems for Safety and Security

Springer Science & Business Media

Field Programmable Gate Arrays (FPGAs) are on the verge of revolutionising digital signal processing. Novel FPGA families are increasingly replacing ASICs and PDSPs for front-end digital signal processing algorithms. The efficient implementation of these algorithms is the main goal of this book. It starts with an overview of today's FPGA technology, devices and tools for designing DSP systems. A case study in the first chapter is the basis for more than 30 design examples. The following chapters deal with topics such as computer arithmetic concepts and the theory and the implementation of FIR and IIR filters. The VERILOG source code and a glossary are contained in the appendices. The accompanying CD-ROM contains examples in VHDL and Verilog code as well as the newest Altera 'Baseline' software.

Field-Programmable Gate Array Technology CRC Press

Focusing on resource awareness in field-programmable gate array (FPGA) design, Applications of Field-Programmable Gate Arrays in Scientific Research covers the principle of FPGAs and their functionality. It explores a host of applications, ranging from small one-chip laboratory systems to large-scale applications in "big science." The book first describes various FPGA resources, including logic elements, RAM, multipliers, microprocessors, and content-addressable memory. It then presents principles and methods for controlling resources, such as process sequencing, location constraints, and intellectual property cores. The remainder of the book illustrates examples of applications in high-energy physics, space, and radiobiology. Throughout the text, the authors remind designers to pay attention to resources at the planning, design, and implementation stages of an FPGA application, in order to reduce the use of limited silicon resources and thereby reduce system cost. Supplying practical know-how on an array of FPGA application examples, this book provides an accessible overview of the use of FPGAs in data acquisition, signal processing, and transmission. It shows how FPGAs are employed in laboratory applications and how they are flexible, low-cost alternatives to commercial data acquisition systems. Web Resource A supporting website at <http://scipp.ucsc.edu/~hartmut/FPGA> offers more details on FPGA programming and usage. The site contains design elements of the case studies from the book, including VHDL code, detailed schematics of selected projects, photographs, and screen shots.

Field-Programmable Gate Arrays Wiley-Interscience

This Edited Volume Field Programmable Gate Arrays (FPGAs) II is a collection of reviewed and relevant research chapters, offering a comprehensive overview of recent developments in the field of Computer and Information Science. The book comprises single chapters authored by various researchers and edited by an expert active in the Computer and Information Science research area. All chapters are complete in itself but united under a common research study topic. This publication aims at providing a thorough overview of the latest research efforts by international authors on Computer and Information Science, and open new possible research paths for further novel developments.

Field-Programmable Gate Array Technology Springer Science & Business Media

An important working resource for engineers and researchers involved in the design, development, and implementation of signal processing systems The last decade has seen a rapid expansion of the use of field programmable gate arrays (FPGAs) for a wide range of applications beyond traditional digital signal processing (DSP) systems. Written by a team of experts working at the leading edge of FPGA research and development, this second edition of FPGA-based Implementation of Signal Processing Systems has been extensively updated and revised to reflect the latest iterations of FPGA theory, applications, and technology. Written from a system-level perspective, it features expert discussions of contemporary methods and tools used in the design, optimization and implementation of DSP systems using programmable FPGA hardware. And it

provides a wealth of practical insights—along with illustrative case studies and timely real-world examples—of critical concern to engineers working in the design and development of DSP systems for radio, telecommunications, audio-visual, and security applications, as well as bioinformatics, Big Data applications, and more. Inside you will find up-to-date coverage of: FPGA solutions for Big Data Applications, especially as they apply to huge data sets The use of ARM processors in FPGAs and the transfer of FPGAs towards heterogeneous computing platforms The evolution of High Level Synthesis tools—including new sections on Xilinx's HLS Vivado tool flow and Altera's OpenCL approach Developments in Graphical Processing Units (GPUs), which are rapidly replacing more traditional DSP systems FPGA-based Implementation of Signal Processing Systems, 2nd Edition is an indispensable guide for engineers and researchers involved in the design and development of both traditional and cutting-edge data and signal processing systems. Senior-level electrical and computer engineering graduates studying signal processing or digital signal processing also will find this volume of great interest.

FPGAs for Software Programmers Maker Media, Inc.

Accidents and natural disasters involving nuclear power plants such as Chernobyl, Three Mile Island, and the recent meltdown at Fukushima are rare, but their effects are devastating enough to warrant increased vigilance in addressing safety concerns. Nuclear Power Plant Instrumentation and Control Systems for Safety and Security evaluates the risks inherent to nuclear power and methods of preventing accidents through computer control systems and other such emerging technologies. Students and scholars as well as operators and designers will find useful insight into the latest security technologies with the potential to make the future of nuclear energy clean, safe, and reliable.

Logic Synthesis and Optimization Springer Science & Business Media

Field-Programmable Gate Array (FPGA) technologies have increased in popularity in recent years due to their adaptability and high computing potential. Further research in this area illustrates the potential for further advancements and applications of this useful technology. Field-Programmable Gate Array (FPGA) Technologies for High Performance Instrumentation presents experimental and theoretical research on FPGA-based design and the development of virtual scientific instrumentation that can be used by a broad segment of scientists across a variety of research fields. Focusing on crucial innovations and algorithms for signal processing, data acquisition mechanisms, FPGA-based hardware design, and parallel computing, this publication is a critical resource for researchers, development engineers, and graduate-level students.

Field-Programmable Gate Array (FPGA) Technologies for High Performance Instrumentation

Prentice Hall

The information infrastructure – comprising computers, embedded devices, networks and software systems – is vital to operations in every sector: chemicals, commercial facilities, communications, critical manufacturing, dams, defense industrial base, emergency services, energy, financial services, food and agriculture, government facilities, healthcare and public health, information technology, nuclear reactors, materials and waste, transportation systems, and water and wastewater systems. Global business and industry, governments, indeed society itself, cannot function if major components of the critical information infrastructure are degraded, disabled or destroyed. Critical Infrastructure Protection XII describes original research results and innovative applications in the interdisciplinary field of critical infrastructure protection. Also, it highlights the importance of weaving science, technology and policy in crafting sophisticated, yet practical, solutions that will help secure information, computer and network assets in the various critical infrastructure sectors. Areas of coverage include: Themes and Issues; Infrastructure Protection; Infrastructure Modeling and Simulation; Industrial Control Systems Security. This book is the twelfth volume in the annual series produced by the International Federation for Information Processing (IFIP) Working Group 11.10 on Critical Infrastructure Protection, an international

community of scientists, engineers, practitioners and policy makers dedicated to advancing research, development and implementation efforts focused on infrastructure protection. The book contains a selection of fifteen edited papers from the Twelfth Annual IFIP WG 11.10 International Conference on Critical Infrastructure Protection, held at SRI International, Arlington, Virginia, USA in the spring of 2018. Critical Infrastructure Protection XII is an important resource for researchers, faculty members and graduate students, as well as for policy makers, practitioners and other individuals with interests in homeland security.

[Learning FPGAs](#) Springer Science & Business Media

Reviews the historical development of programmable logic devices, the fundamental programming technologies that the programmability is built on, and then describes the basic understandings gleaned from research on architectures. It is an invaluable reference for engineers and computer scientists.

[Field Programmable Logic and Application](#) Springer

This is the most comprehensive practical guide to designing with FPGAs and programmable LSI. Programmable logic devices (PLDs) have been in general use for over twenty years. The demands of modern electronic design mean that traditional PAL technology has given way to a powerful new approach: field programmable gate array (FPGA) technology. This book shows a designer how to choose the appropriate FPGA and design method for any application. It also gives hints and tips, based on the author's wide experience in the field, to allow designers to optimise performance of any particular family of devices. The design process for an FPGA needs to be far more rigorous than for PAL since troubleshooting is far harder to perform. There are, moreover, a dozen or more manufacturers of FPGAs, each with a different architecture and performance, so choosing the right device for any particular application is a critical part of the design process. Similarly there are various design methods, each with particular features. This book covers them all and gives a designer the benefit of Geoff Bostock's valuable experience. First design guide published on FPGAs Full of data never previously compiled Based on real design experience of making FPGAs and programmable LSI work

[Digital Systems Design with FPGAs and CPLDs](#) Springer Science & Business Media

Due to unique advantages like security, improved testing, and reprogrammability, field programmable gate arrays are making broad inroads in the electronics industry. This comprehensive overview of the topic explains the underlying principles, strengths and limitations of a range of FPGA architectures. Includes abundant references and illustrations.

[The Design Warrior's Guide to FPGAs](#) Springer Science & Business Media

Learn how to design digital circuits with FPGAs (field-programmable gate arrays), the devices that reconfigure themselves to become the very hardware circuits you set out to program. With this practical guide, author Justin Rajewski shows you hands-on how to create FPGA projects, whether you're a programmer, engineer, product designer, or maker. You'll quickly go from the basics to designing your own processor. Designing digital circuits used to be a long and costly endeavor that only big companies could pursue. FPGAs make the process much easier, and now they're affordable enough even for hobbyists. If you're familiar with electricity and basic electrical components, this book starts simply and progresses through increasingly complex projects. Set up your environment by installing Xilinx ISE and the author's Mojo IDE Learn how hardware designs are broken into modules, comparable to functions in a software program Create digital hardware designs and learn the basics on how they'll be implemented by the FPGA Build your projects with Lucid, a beginner-friendly hardware description language, based on Verilog, with syntax similar to C/C++ and Java

[FPGAs and Programmable LSI](#) Elsevier

Many different kinds of FPGAs exist, with different programming technologies, different architectures and different software. Field-Programmable Gate Array Technology describes the major FPGA architectures available today, covering the three programming technologies that are in use and the major architectures built on those programming technologies. The reader is introduced to concepts relevant to the entire field of FPGAs using popular devices as examples.

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[Critical Infrastructure Protection XII](#) Springer Science & Business Media

The Knowledge Solution. Stop Searching, Stand Out and Pay Off. The #1 ALL ENCOMPASSING Guide to FPGA Field-Programmable Gate Array. An Important Message for ANYONE who wants to learn about FPGA Field-Programmable Gate Array Quickly and Easily... ""Here's Your Chance To Skip The Struggle and Master FPGA Field-Programmable Gate Array, With the Least Amount of Effort, In 2 Days Or Less..."" A field-programmable gate array (FPGA) is an integrated circuit designed to be configured by the customer or designer after manufacturing-hence ""field-programmable."" The FPGA configuration is generally specified using a hardware description language (HDL), similar to that used for an application-specific integrated circuit (ASIC) (circuit diagrams were previously used to specify the configuration, as they were for ASICs, but this is increasingly rare). FPGAs can be used to implement any logical function that an ASIC could perform. The ability to update the functionality after shipping, partial re-configuration of the portion of the design and the low non-recurring engineering costs relative to an ASIC design (notwithstanding the generally higher unit cost), offer advantages for many applications. Get the edge, learn EVERYTHING you need to know about FPGA Field-Programmable Gate Array, and ace any discussion, proposal and implementation with the ultimate book - guaranteed to give you the education that you need, faster than you ever dreamed possible! The information in this book can show you how to be an expert in the field of FPGA Field-Programmable Gate Array. Are you looking to learn more about FPGA Field-Programmable Gate Array? You're about to discover the most spectacular gold mine of FPGA Field-Programmable Gate Array materials ever created, this book is a unique collection to help you become a master of FPGA Field-Programmable Gate Array. This book is your ultimate resource for FPGA Field-Programmable Gate Array. Here you will find the most up-to-date information, analysis, background and everything you need to know. In easy to read chapters, with extensive references and links to get you to know all there is to know about FPGA Field-Programmable Gate Array right away. A quick look inside: Field-programmable gate array, Gate array, Application-specific instruction-set processor, Application-specific integrated circuit, C-slowng, Complex programmable logic device, Delay-locked loop, Digital Clock Manager, Digitally controlled impedance, DIME-C, Erasable programmable logic device, Field Programmable Nanowire Interconnect, Field-programmable analog array, FPGA prototype, Generic array logic, Macrocell array, Partial re-configuration, Programmable Array Logic, Programmable logic device, Programmable system device, Rent's rule, Sopc builder, ZX8301, ZX8302 ...and Much, Much More! This book explains in-depth the real drivers and workings of FPGA Field-Programmable Gate Array. It reduces the risk of your technology, time and resources investment decisions by enabling you to compare your understanding of FPGA Field-Programmable Gate Array with the objectivity of experienced professionals - Grab your copy now, while you still can.

[FPGA-based Implementation of Signal Processing Systems](#) Springer

Short turnaround has become critical in the design of electronic systems. Software- programmable components such as microprocessors and digital signal processors have been used extensively in such systems since they allow rapid design revisions. However, the inherent performance limitations of software-programmable systems mean that they are inadequate for high-performance designs. Designers thus turned to gate arrays as a solution. User-programmable gate arrays (field-programmable gate arrays, FPGAs) have recently emerged and are changing the way

electronic systems are designed and implemented. The growing complexity of the logic circuits that can be packed onto an FPGA chip means that it has become important to have automatic synthesis tools that implement logic functions on these architectures. Logic Synthesis for Field-Programmable Gate Arrays describes logic synthesis for both look-up table (LUT) and multiplexor-based architectures, with a balanced presentation of existing techniques together with algorithms and the system developed by the authors. Audience: A useful reference for VLSI designers, developers of computer-aided design tools, and anyone involved in or with FPGAs.

[FPGA Programming for Beginners](#) Now Publishers Inc

The push to move products to market as quickly and cheaply as possible is fiercer than ever, and accordingly, engineers are always looking for new ways to provide their companies with the edge over the competition. Field-Programmable Gate Arrays (FPGAs), which are faster, denser, and more cost-effective than traditional programmable logic devices (PLDs), are quickly becoming one of the most widespread tools that embedded engineers can utilize in order to gain that needed edge. FPGAs are especially popular for prototyping designs, due to their superior speed and efficiency. This book hones in on that rapid prototyping aspect of FPGA use, showing designers exactly how they can cut time off production cycles and save their companies money drained by costly mistakes, via prototyping designs with FPGAs first. Reading it will take a designer with a basic knowledge of implementing FPGAs to the "next-level of FPGA use because unlike broad beginner books on FPGAs, this book presents the required design skills in a focused, practical, example-oriented manner. In-the-trenches expert authors assure the most applicable advice to practicing engineers Dual focus on successfully making critical decisions and avoiding common pitfalls appeals to engineers pressured for speed and perfection Hardware and software are both covered, in order to address the growing trend toward "cross-pollination" of engineering expertise

[Implementing Self-timed Circuits in Field Programmable Gate Arrays](#) Elsevier

Explore a comprehensive and state-of-the-art presentation of real-time electromagnetic transient simulation technology by leaders in the field Real-Time Electromagnetic Transient Simulation of AC-DC Networks delivers a detailed exposition of field programmable gate array (FPGA) hardware based real-time electromagnetic transient (EMT) emulation for all fundamental equipment used in AC-DC power grids. The book focuses specifically on detailed device-level models for their hardware realization in a massively parallel and deeply pipelined manner as well as decomposition techniques for emulating large systems. Each chapter contains fundamental concepts, apparatus models, solution algorithms, and hardware emulation to assist the reader in understanding the material contained within. Case studies are peppered throughout the book, ranging from small didactic test circuits to realistically sized large-scale AC-DC grids. The book also provides introductions to FPGA and hardware-in-the-loop (HIL) emulation procedures, and large-scale networks constructed by the foundational components described in earlier chapters. With a strong focus on high-voltage direct-current power transmission grid applications, Real-Time Electromagnetic Transient Simulation of AC-DC Networks covers both system-level and device-level mathematical models. Readers will also enjoy the inclusion of: A thorough introduction to field programmable gate array technology, including the evolution of FPGAs, technology trends, hardware architectures, and programming tools An exploration of classical power system components, e.g., linear and nonlinear passive power system components, transmission lines, power transformers, rotating machines, and protective relays A comprehensive discussion of power semiconductor switches and converters, i.e., AC-DC and DC-DC converters, and specific power electronic apparatus such as DC circuit breakers An examination of decomposition techniques used at the equipment-level as well as the large-scale system-level for real-time EMT emulation of AC-DC networks Chapters that are supported by simulation results from well-defined test cases and the corresponding system parameters are provided in the Appendix Perfect for graduate students and professional engineers studying or working in electrical power engineering, Real-Time Electromagnetic Transient Simulation of AC-DC Networks will also earn a place in the libraries of simulation specialists, senior modeling and simulation engineers, planning and design engineers, and system studies engineers.